





The problem numbers are from your text book (both the 6th and 7th edition will work)

- 2) Simplified Problem 7.35 (a-ii): Repeat the previous problem by using only two-input NOR gates. Add inverters where necessary

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- 3) Simplified Problem 7.36 (b): Realize the following function using only two-input NAND gates. Add inverters where necessary

$$F = (B + (A' + C')D) (A(B' + D') + C)$$

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- 4) Simplified Problem 7.36 (c): Repeat the previous problem by using only two-input NOR gates.

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The problem numbers are from your text book (both the 6th and 7th edition will work)

5) Problem 7.37: Realize the following function using NOR gates. Add inverters where necessary

$$Z = A[BC' + D + E(F' + GH)]$$